

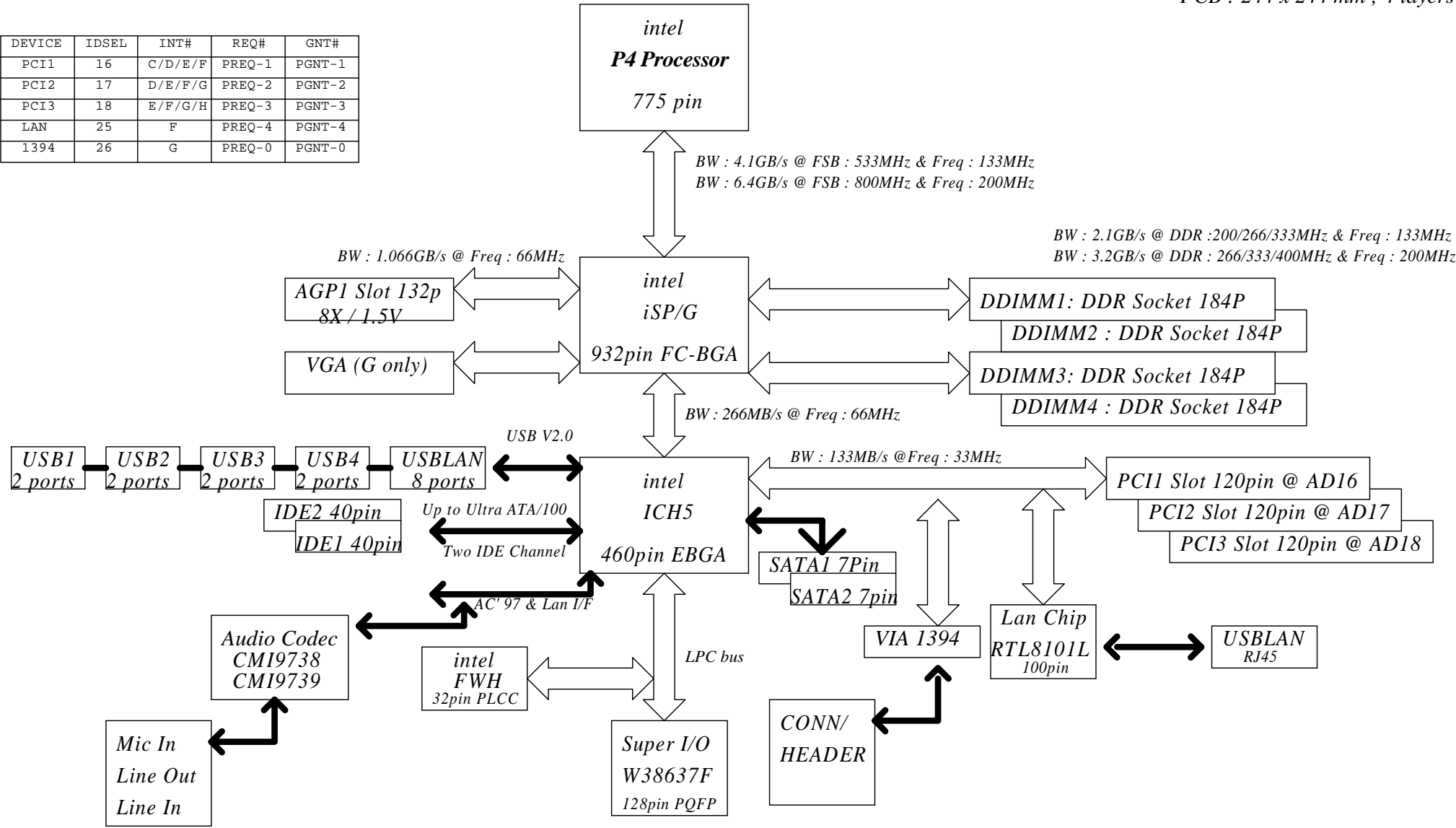
Schematics Version History Table :

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1.0	1.0	35		09/02 / ' 04
1.1	1.1	35		11/15 / ' 04

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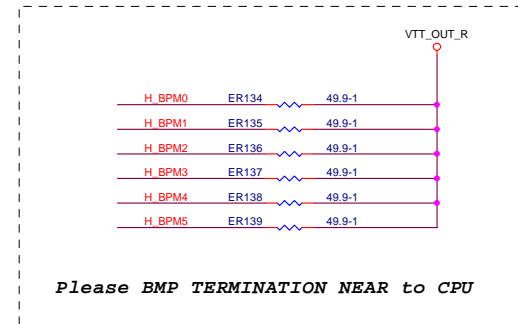
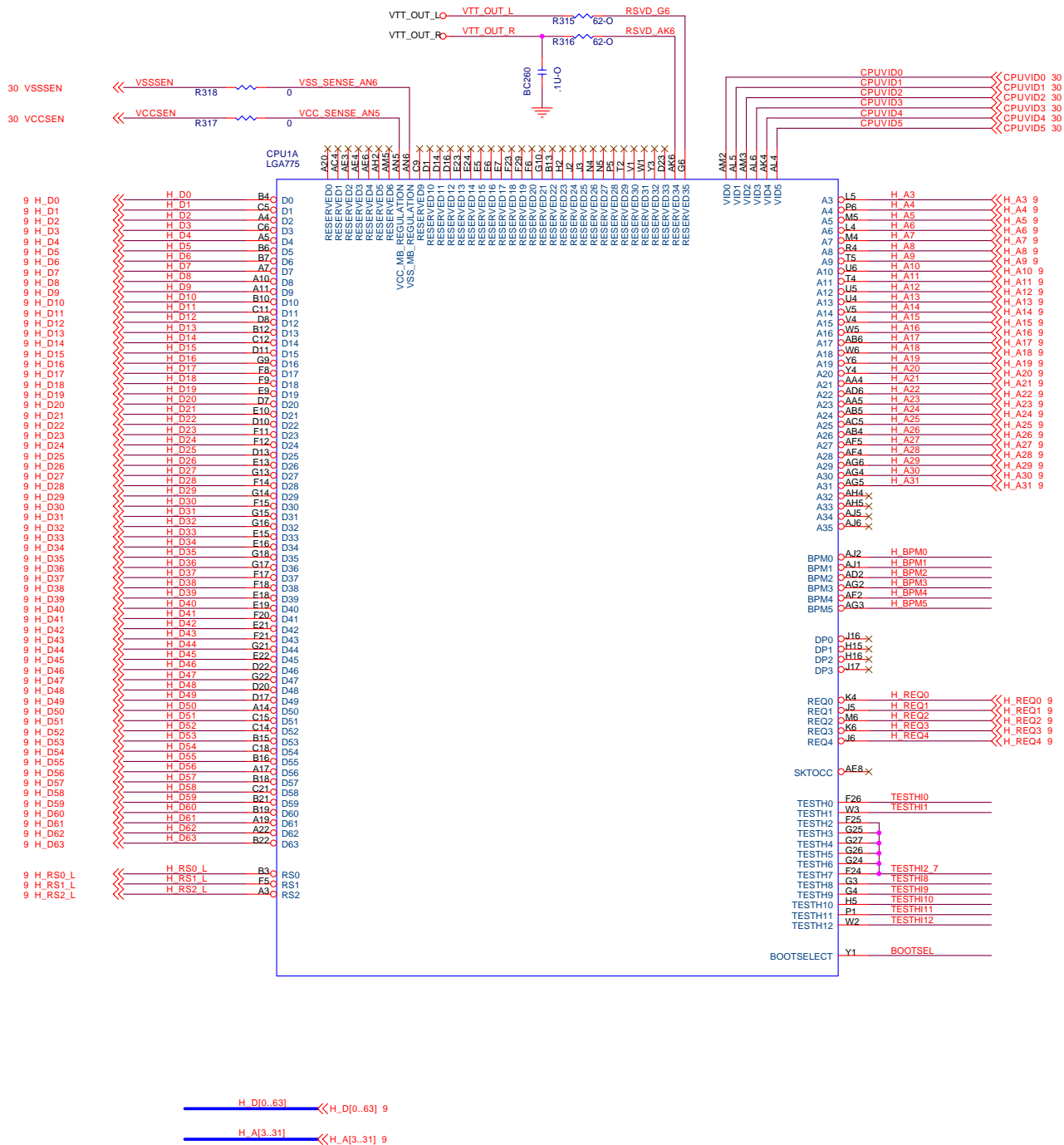
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DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	16	C/D/E/F	PREQ-1	PGNT-1
PCI2	17	D/E/F/G	PREQ-2	PGNT-2
PCI3	18	E/F/G/H	PREQ-3	PGNT-3
LAN	25	F	PREQ-4	PGNT-4
1394	26	G	PREQ-0	PGNT-0

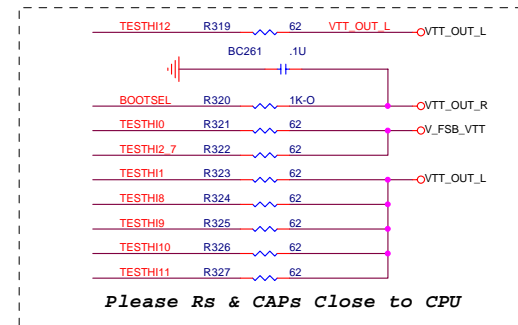


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Title			System Block Diagram	
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Please BMP TERMINATION NEAR to CPU

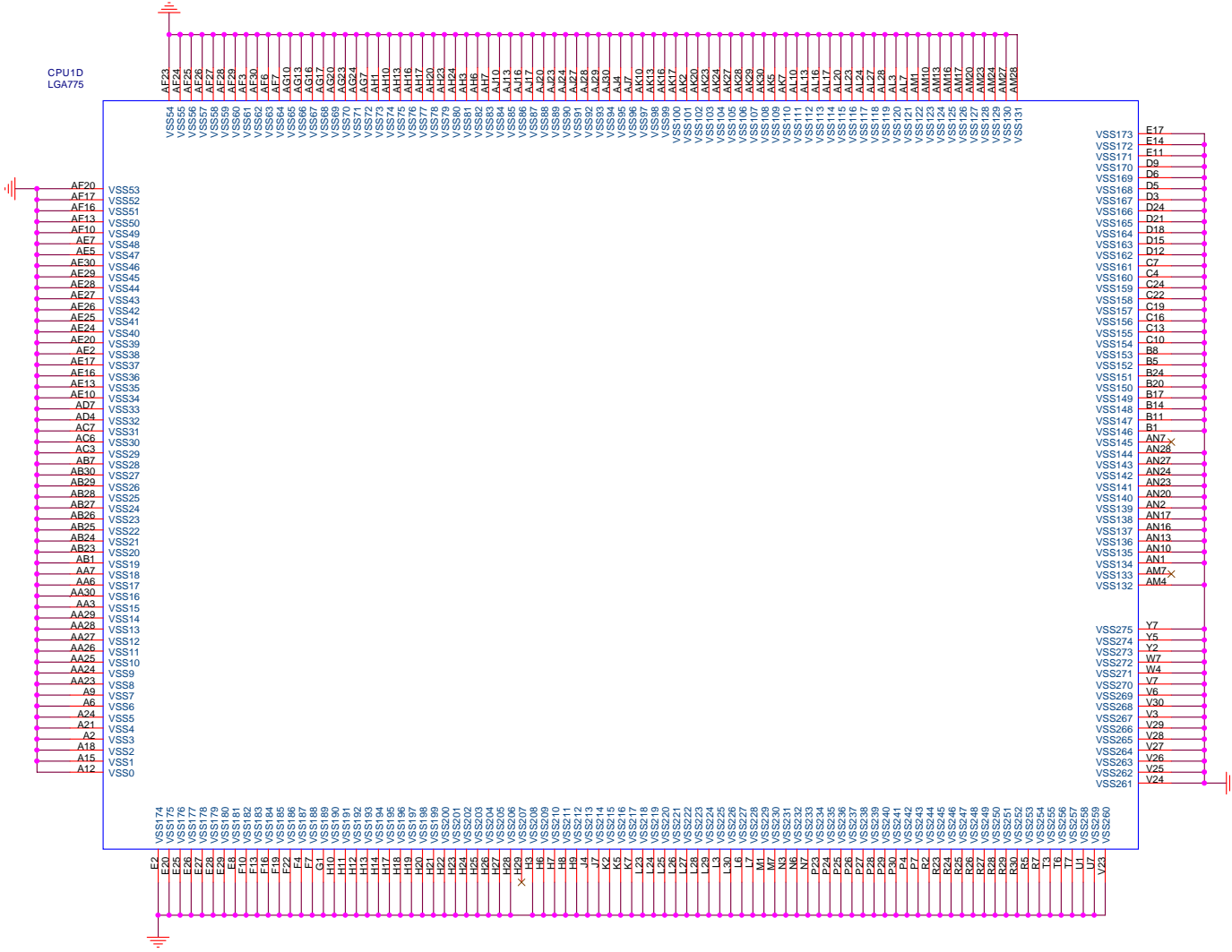


Please Rs & CAPs Close to CPU



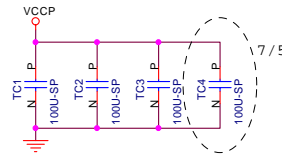
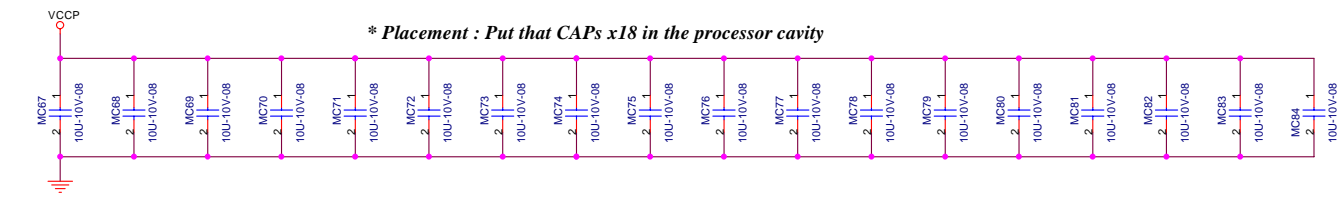
CPU1C
LGA775

CPU1D
LGA775



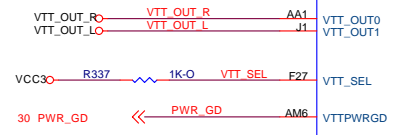
litegroup Computer Systems

Title			P4 LGA775P Part D	
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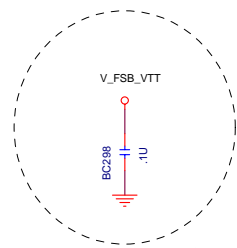
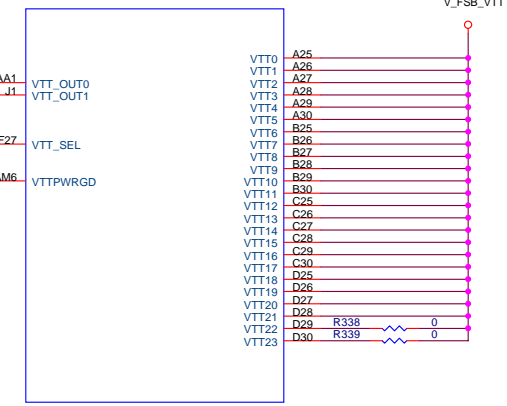


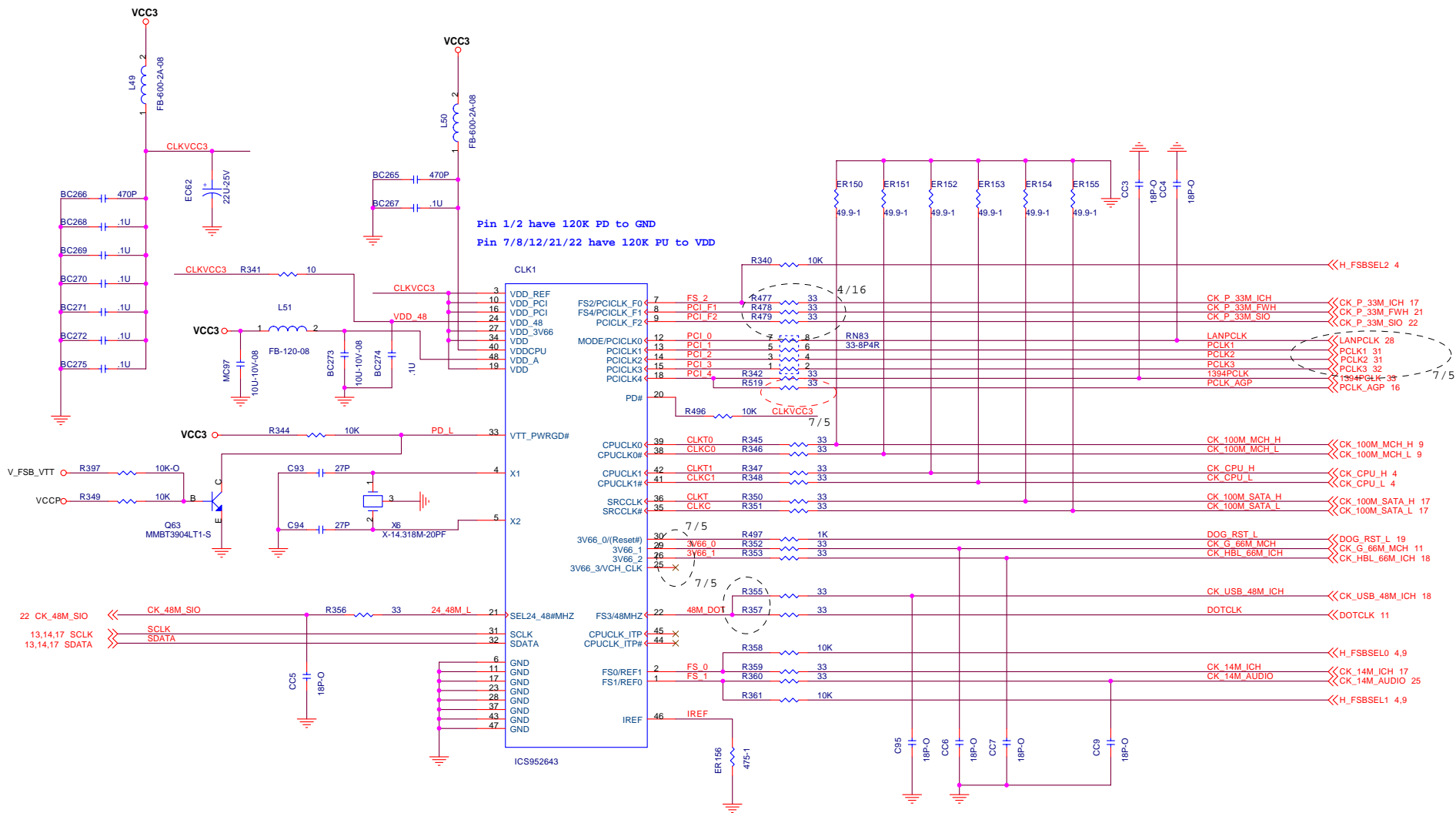
MC109,MC110 near to pin V26 & U27

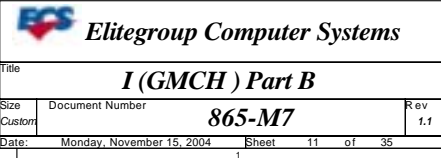
VTT_SEL=0 for the Tejas processor

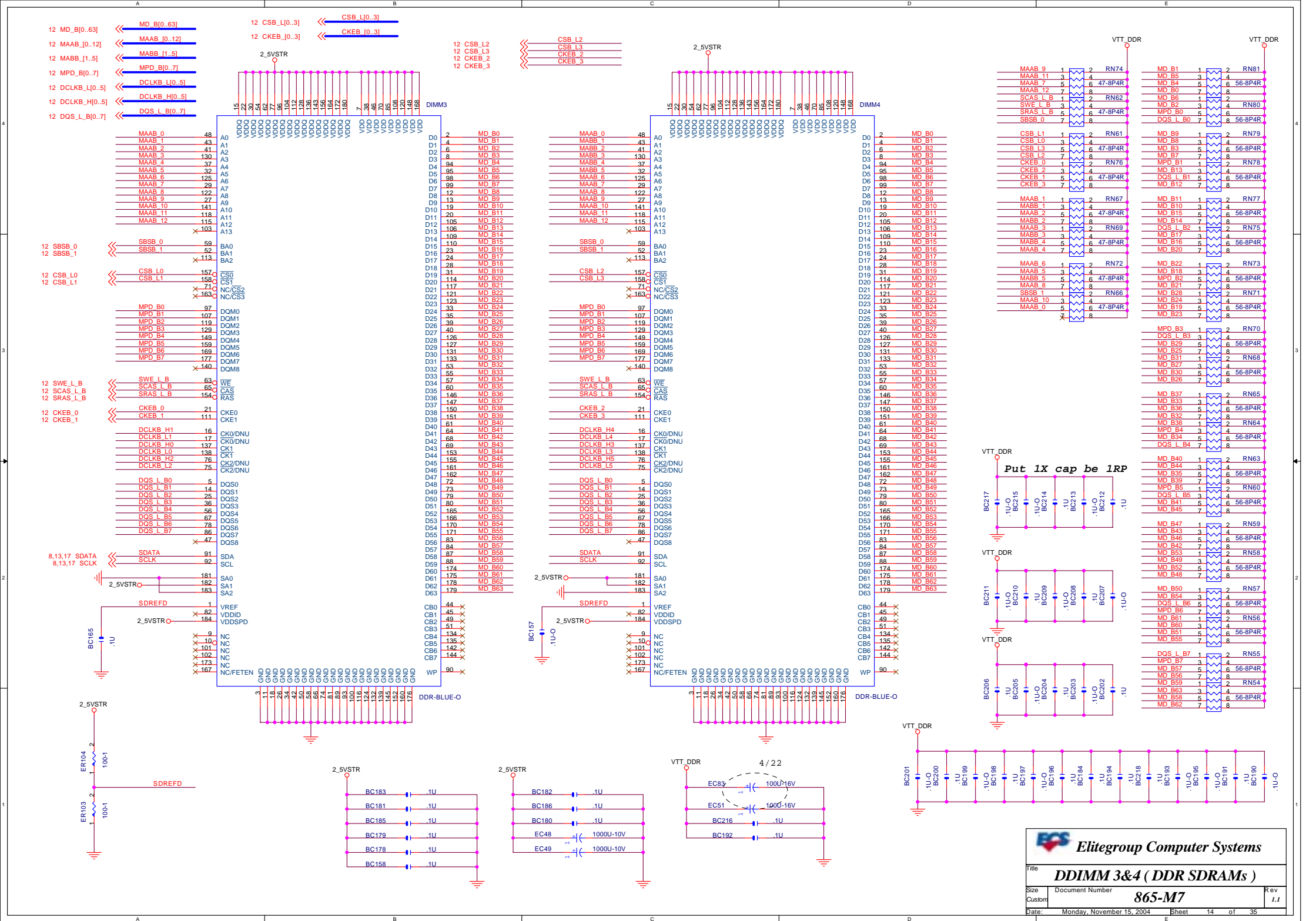


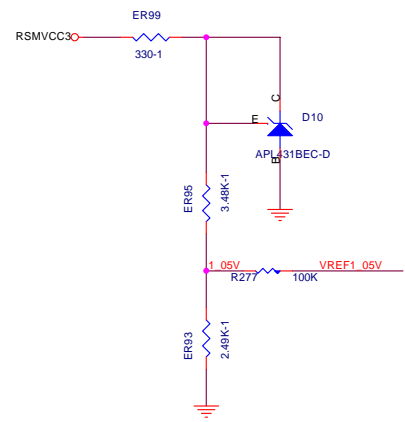
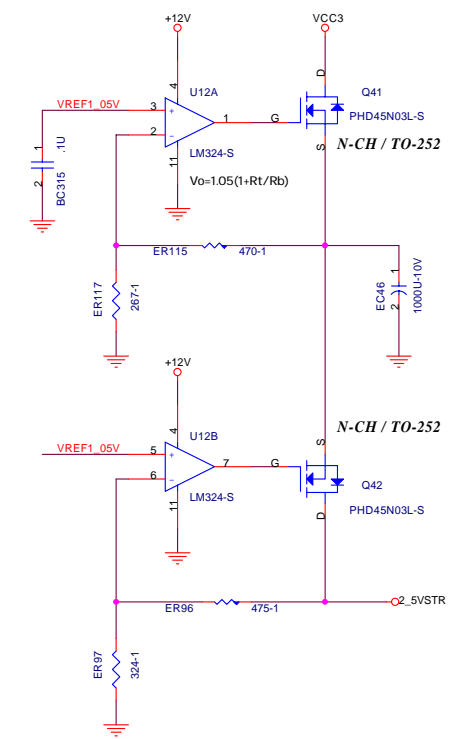
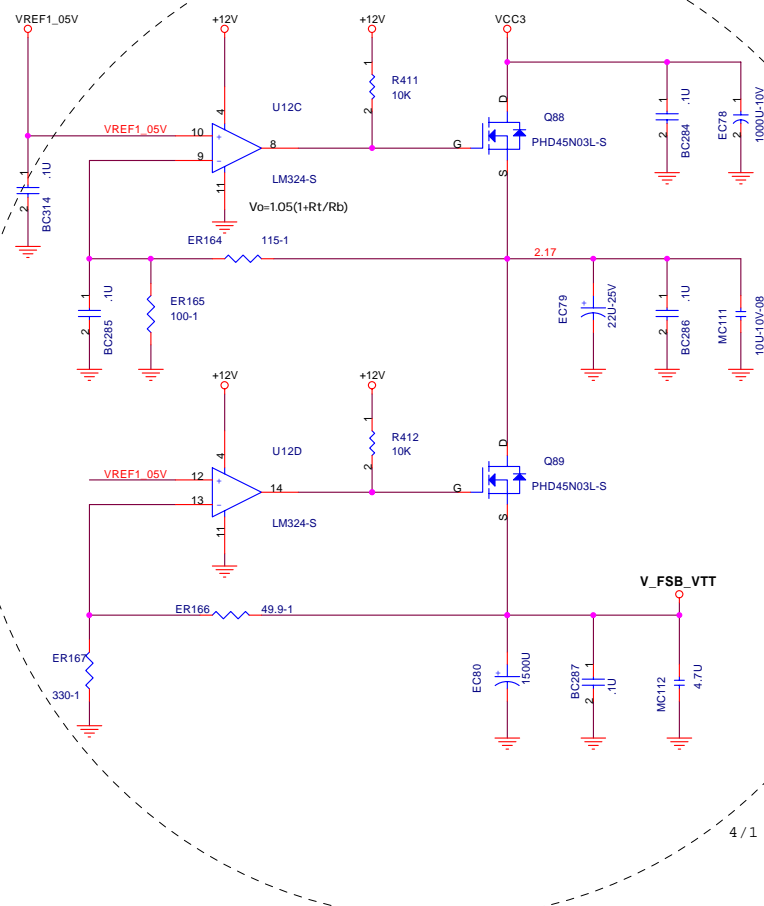
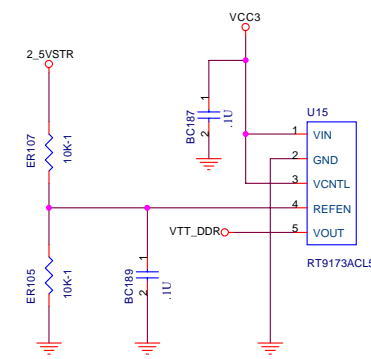
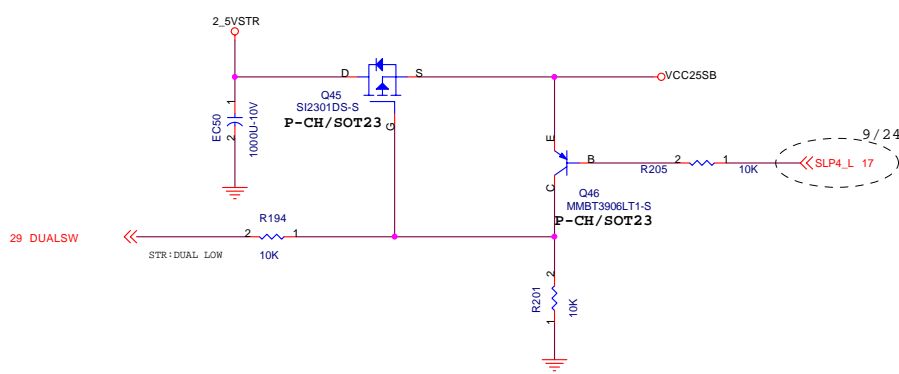
CPU1E
LGA775

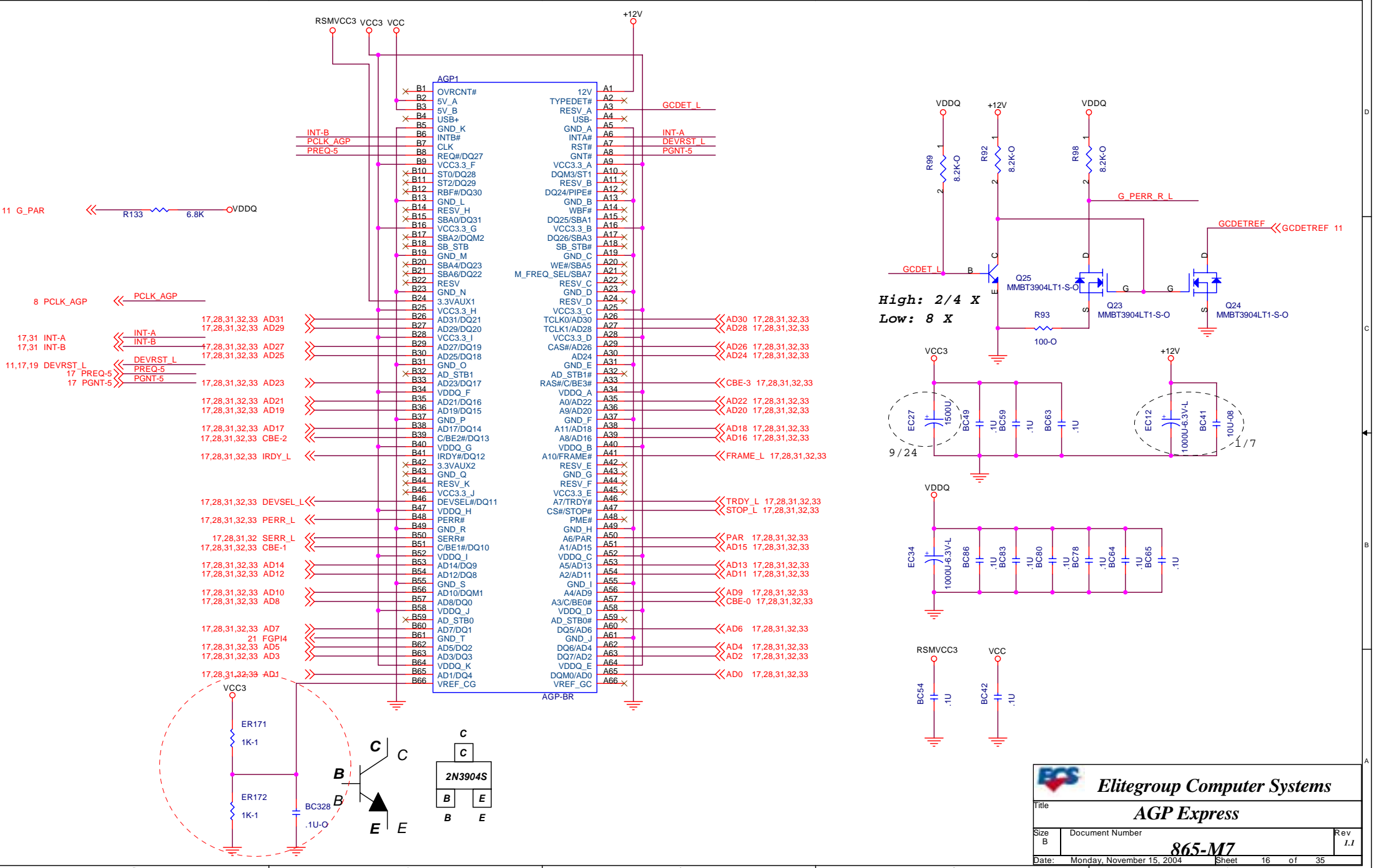







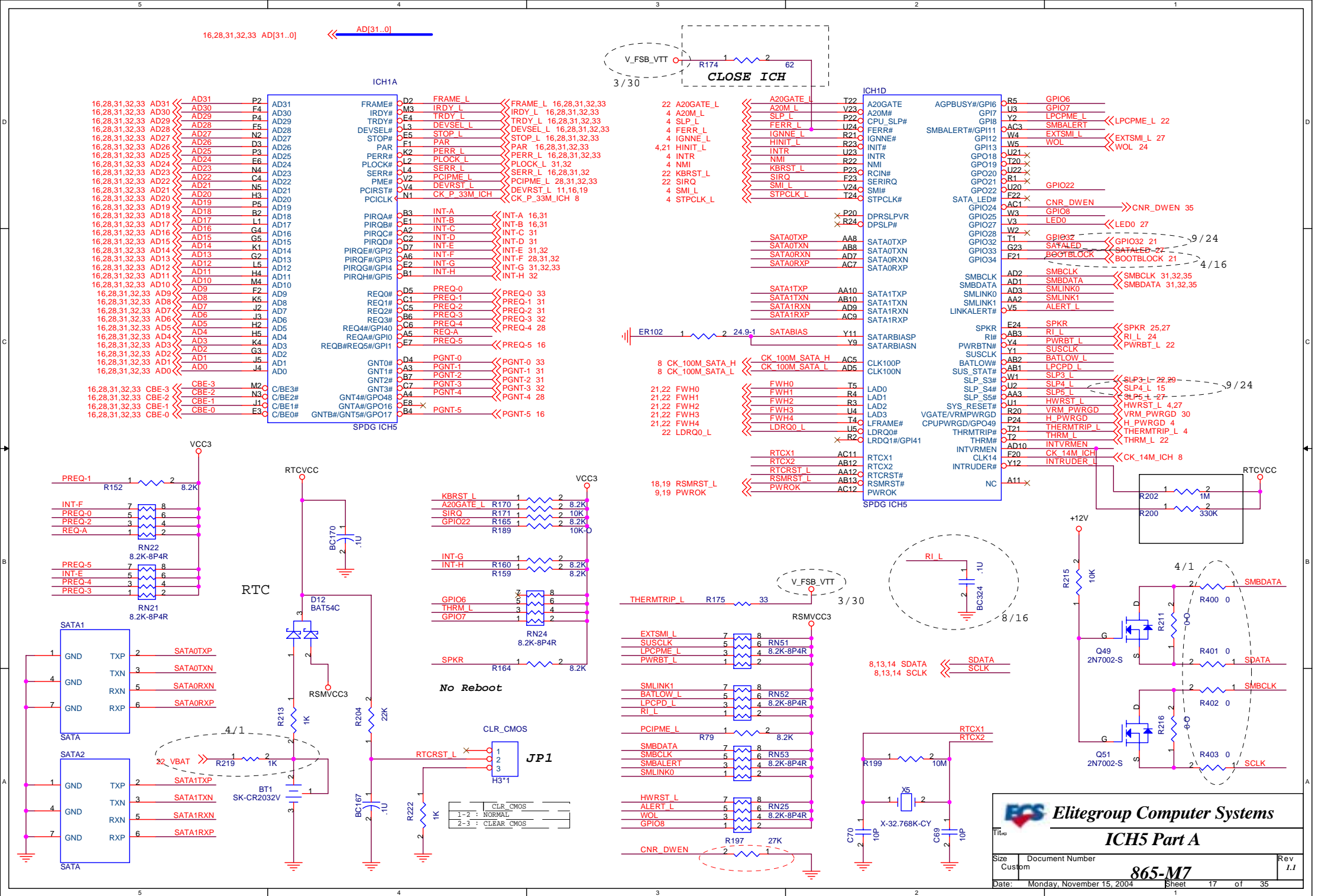


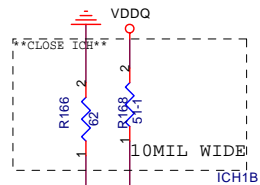




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HL[0..10] << HL[0..10] 11

20 PDD[0..15] << PDD[0..15]

20 SDD[0..15] << SDD[0..15]

11 HL0
11 HL1
11 HL2
11 HL3
11 HL4
11 HL5
11 HL6
11 HL7
11 HL8
11 HL9
11 HL10

11 HLSTB_H
11 HLSTB_L
11 HBSWING
11 HUBREF
8 CK_HBL_66M_ICK

35 CLANRXD0
35 CLANRXD1
35 CLANRXD2
35 CLANRXD3
35 CLANTXD0
35 CLANTXD1
35 CLANTXD2
35 CLANTXD3
35 CLANRSTSYNC
35 CLANCLK
17,19 RSMRST_L
35 EE_DIN
35 EE_CS
35 EE_DOUT
35 EE_SHCLK

25,35 AC_SYNC
35 AC_RST_L
25,35 AC_SDO
35 AC_SDIN0
35 AC_SDIN1
25,35 AC_SDIN2
25,35 AC_BIT_CLK

H20
H21
J20
H22
M23
M21
N21
M20
J22
H23
H24
H25
H26
H27
H28
H29
K21
G22
H210
H211

K23
J24
N24
L20
L24
N22
CLK66

LAN_RXD0
LAN_RXD1
LAN_RXD2
LAN_TXD0
LAN_TXD1
LAN_TXD2
LAN_RSTSYNC
LAN_CLK
LAN_RST#

EE_DIN
EE_CS
EE_DOUT
EE_SHCLK
EE_DOUT

AC_SYNC
AC_RST#
AC_SDO
AC_SDIN0
AC_SDIN1
AC_SDIN2
AC_BIT_CLK

USBP0P
USBP0N
A22 USBP1P
B22 USBP1N
C21 USBP2P
D21 USBP2N
A20 USBP3P
B20 USBP3N
C19 USBP4P
D19 USBP4N
A18 USBP5P
B18 USBP5N
C17 USBP6P
D17 USBP6N
A16 USBP7P
B16 USBP7N

OC0#
OC1#
OC2#
OC3#
OC4#/GPI9
OC5#/GPI10
OC6#/GPI14
OC7#/GPI15

USBRBIAS
USBRBIAS#
CLK48

OC0
D15
D14
C14
B14
A14
D13
C13
A24
B24
F24

CK_USB_48M_ICK << CK_USB_48M_ICK 8

SPDG ICH5

Must routing a trace to 22.6ohm pad, Don't direct connect with USBRBIAS_L.(two trace must < 500 mils)

20 PDD15
20 PDD14
20 PDD13
20 PDD12
20 PDD11
20 PDD10
20 PDD9
20 PDD8
20 PDD7
20 PDD6
20 PDD5
20 PDD4
20 PDD3
20 PDD2
20 PDD1
20 PDD0

20 PDIOW_L
20 PDDACK_L
20 PDREQ_L
20 PDIOR_L
20 PIORDY

20 PDA2
20 PDA1
20 PDA0

20 CS3P_L
20 CS1P_L

20 IRQ14

ICH1C

PDD15 AB17
PDD14 AA16
PDD13 Y16
PDD12 AC16
PDD11 AD15
PDD10 AD16
PDD9 Y15
PDD8 AD15
PDD7 AB14
PDD6 AD14
PDD5 AC15
PDD4 AA14
PDD3 AC14
PDD2 Y14
PDD1 Y13
PDD0 AB16

PDIOW_L AA17
PDDACK_L AC18
PDREQ_L AC17
PDIOR_L AD18
PIORDY AA18

PDA2 AC19
PDA1 AD19
PDA0 AA19

CS3P_L Y18
CS1P_L AB19
IRQ14 Y17

SPDG ICH5

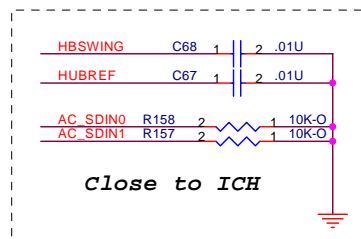
SDD15 AA23
SDD14 AB24
SDD13 AC24
SDD12 AD22
SDD11 AA20
SDD10 AD22
SDD9 Y19
SDD8 AC20
SDD7 AB20
SDD6 AC21
SDD5 AB21
SDD4 AD24
SDD3 AD23
SDD2 AB23
SDD1 AA22
SDD0

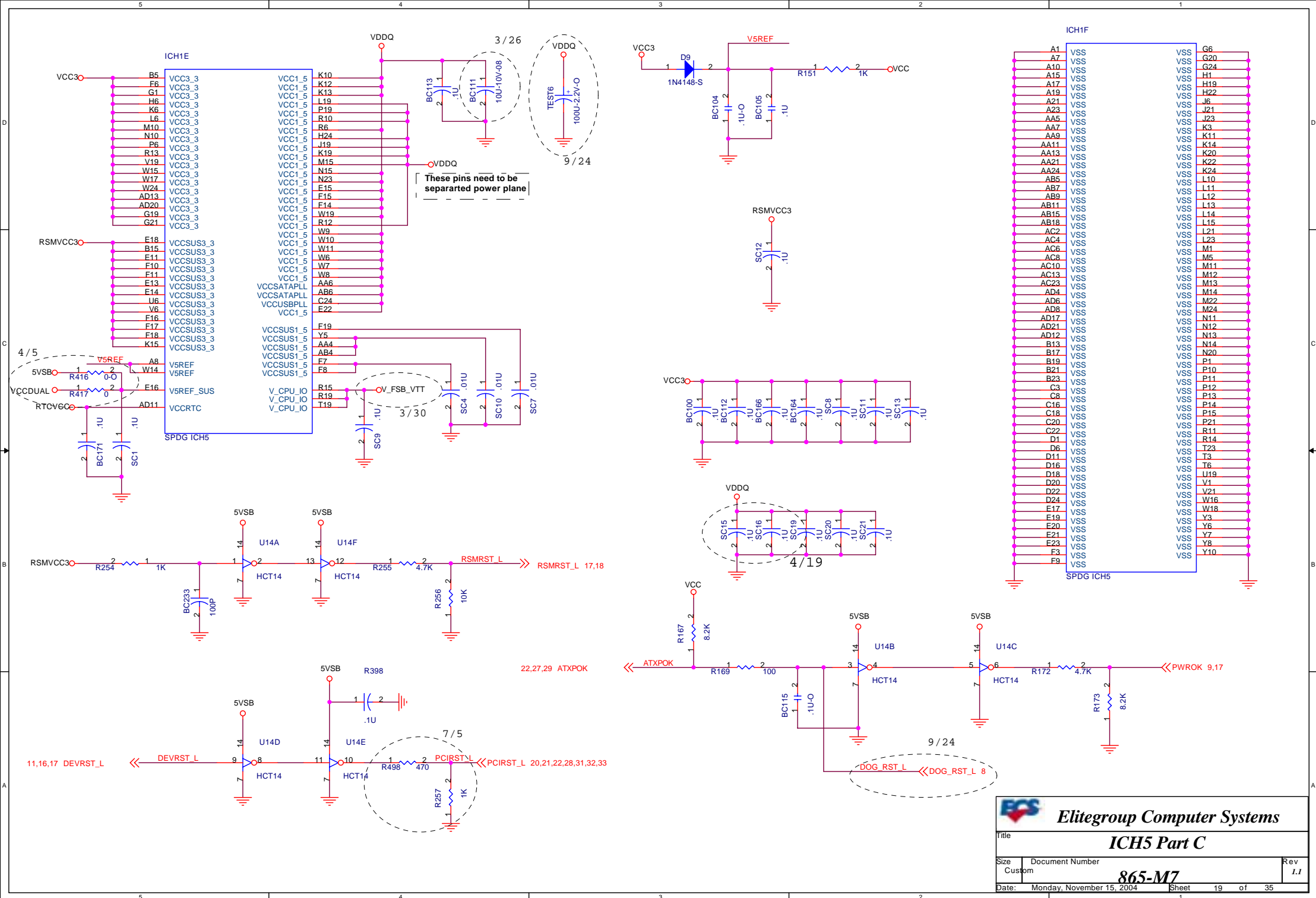
SDIOW_L Y22
SDDACK_L W20
SDREQ_L Y20
SDIOR_L Y23
SIORDY Y21

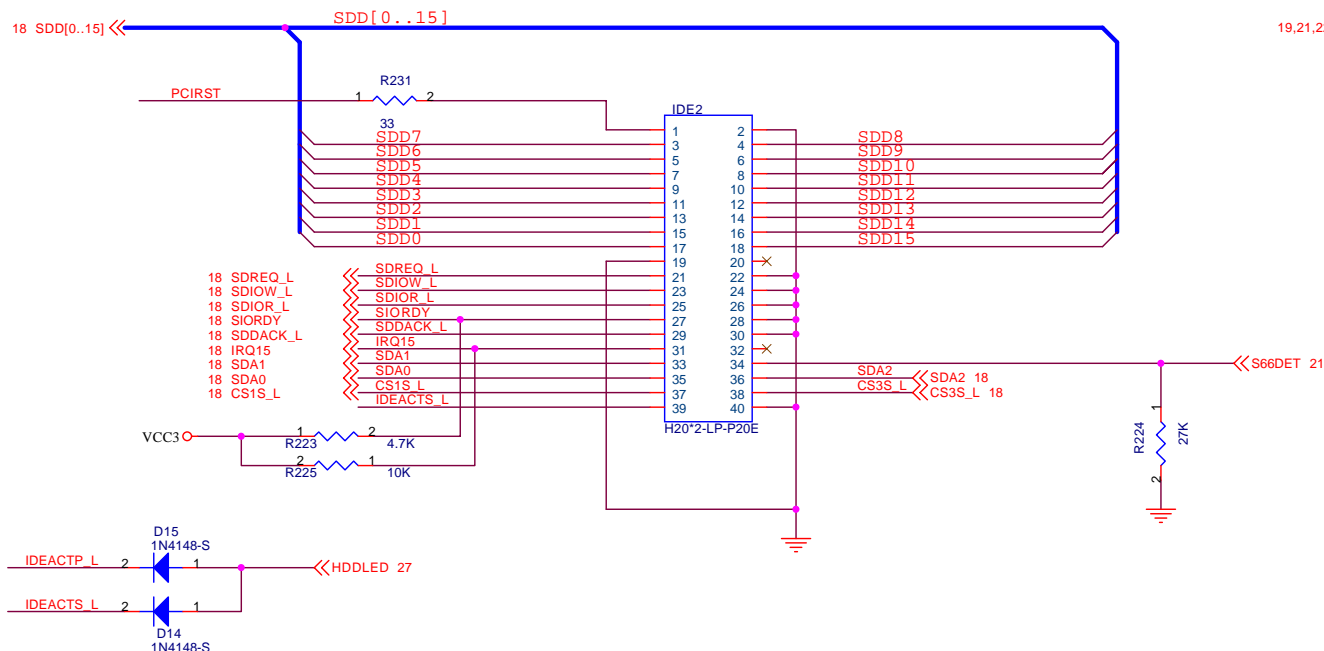
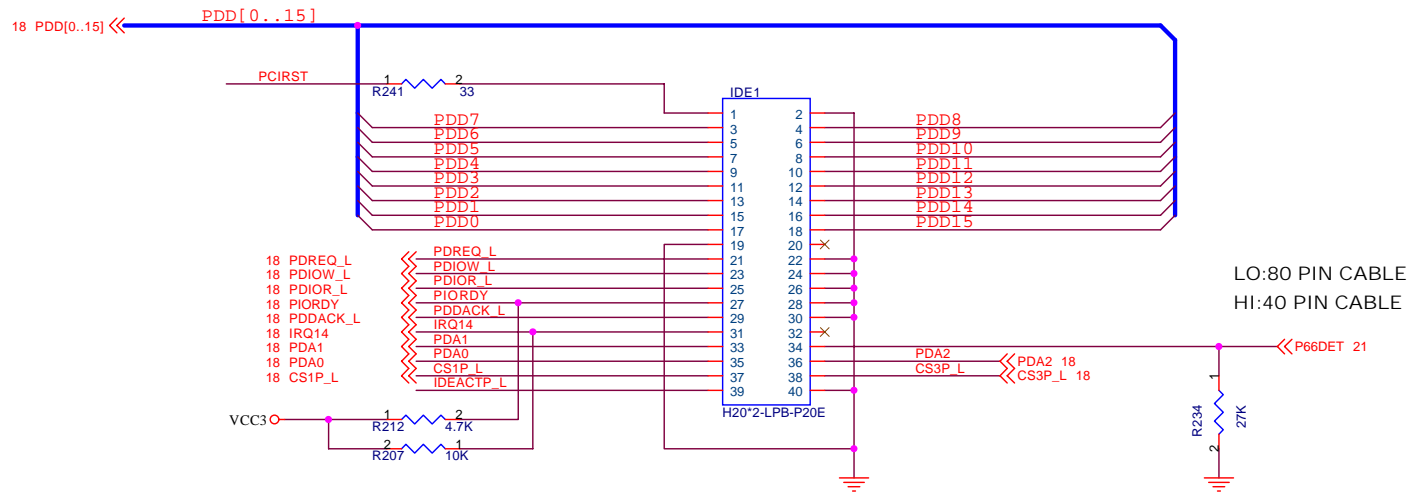
SDA2 W21
SDA1 W23
SDA0 W22

CS3S_L V20
CS1S_L V22
IRQ15 Y24

SPDG ICH5

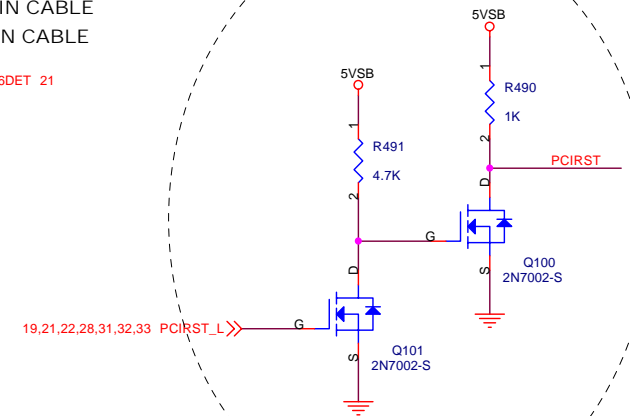







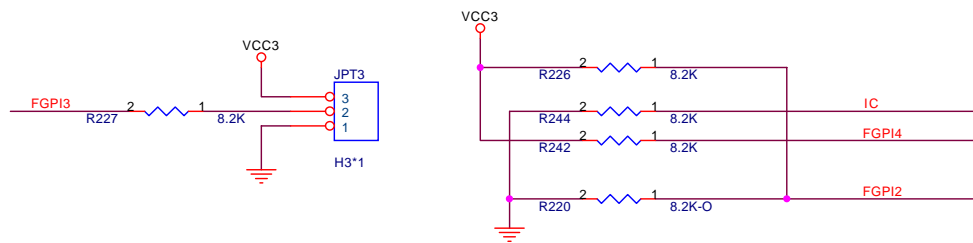
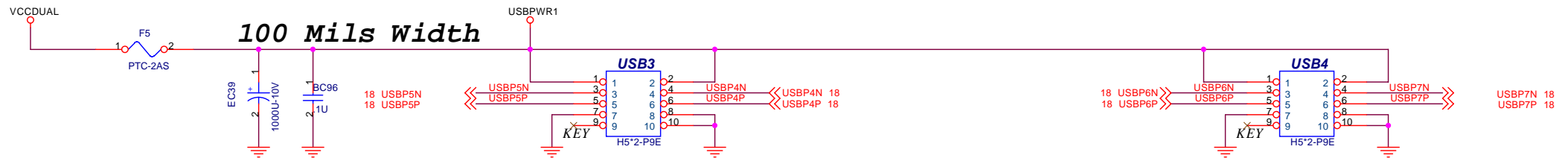
MAX TRACE LENGTH IS 8"

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IDE Connector		
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USB PORT INTERFACE



GPI2:

High:1394 Install

Low:1394 xxx

GPI3:

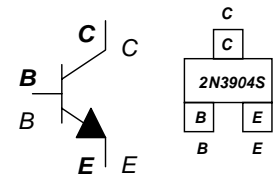
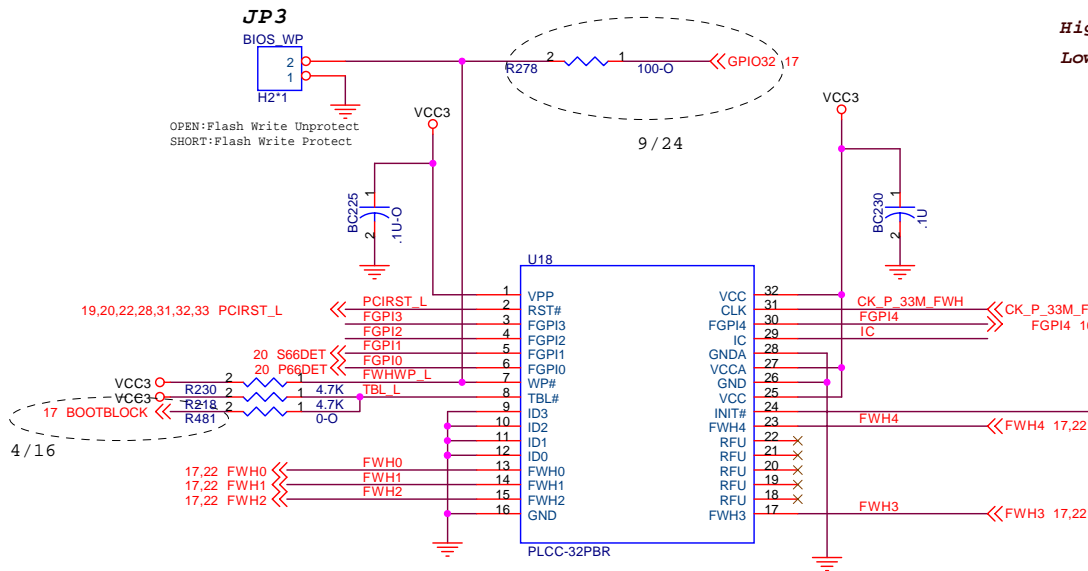
High:Suspend xxx

Low:suspend install

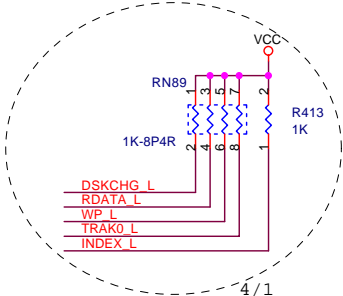
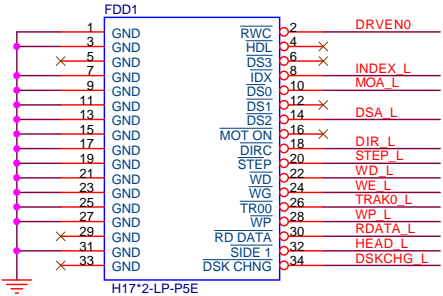
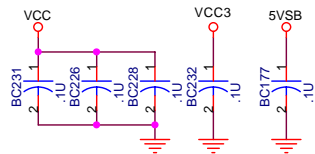
GPI4:

High:3-COM Lan Install

Low:Realtek PCI Lan install



USB/FWH		
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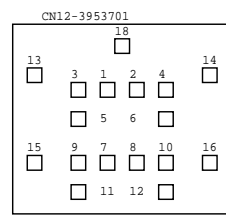
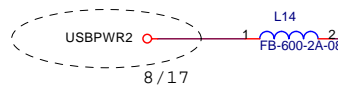
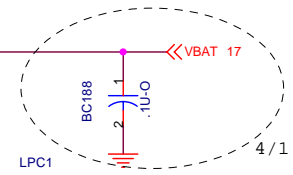
19,20,21,28,31,32,33 PCIRST_L
 8 CK_P_33M_SIO
 17 SIRO
 17 LDRQ0_L
 17,21 FWH4
 17,21 FWH0
 17,21 FWH1
 17,21 FWH2
 17,21 FWH3

PCIRST_L
 CK_P_33M_SIO
 SIRO
 LDRQ0_L
 FWH4
 FWH0
 FWH1
 FWH2
 FWH3

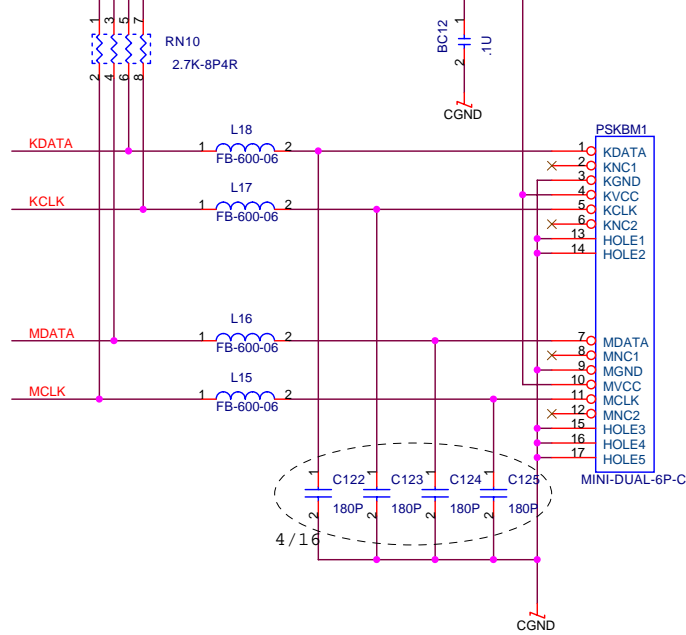
LRESET#
 LCLK
 SERIRQ
 LDRQ#
 LFRAME#
 LAD0
 LAD1
 LAD2
 LAD3

DRVEN0
 DRVEN1
 INDEX#
 MOA#
 DSB#
 DSA#
 MOB#
 DIR#
 STEP#
 WRDATA#
 VEE#
 TRACK#
 WP#
 RDDATA#
 HEAD#
 DSKCHG#

DRVEN0
 INDEX L
 MOA L
 FAN3
 DSA L
 DIR L
 STEP L
 WD L
 WE L
 TRAKO L
 WP L
 RDATA L
 HEAD L
 DSKCHG L



USBPWR2 8/17



24 VREF
 4,24 CPU_THERM1
 24 SYS_THERM

VREF
 CPU_THERM1
 SYS_THERM
 VCC3
 VDDC
 VCCP

24 FANPWM1
 24 FAN1
 24 FANPWM2
 24 FAN2
 17 THRM_L

24 CI_L
 17 LPCPME_L
 27 BEEP

17 PWRBT_L
 27 PWRSW
 27 LED1
 27 PS_ON_L
 17,29 SLP3_L
 8 CK_48M_SIO

FANPWM1
 FAN1
 FANPWM2
 FAN2
 THRM_L
 CI_L
 LPCPME_L
 BEEP
 PWRBT_L
 PWRSW
 LED1
 PS_ON_L
 SLP3_L
 CK_48M_SIO

GamePort

HR Monitor

PD0
 PD1
 PD2
 PD3
 PD4
 PD5
 PD6
 PD7
 SLCT
 PE
 BUSY
 ACK#
 SLIN#
 INIT#
 ERR#
 AFD#
 STB#

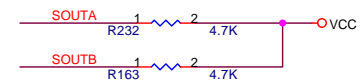
IRRX/GP34
 GP45
 IRTX
 GP40

DCDA#
 DSRA#
 SINA
 RTSA#
 SOUTA
 CTSA#
 DTRA#
 RIA#
 DCDB#
 DSRB#
 SINB
 RTSB#
 SOUTB
 CTSB#
 DTRB#
 RIB#

GA20
 KBRST
 KBDATA
 KBCLK
 MSDATA
 MCLK

RSMRST#/GP44
 PWROK/GP43

W83627THF

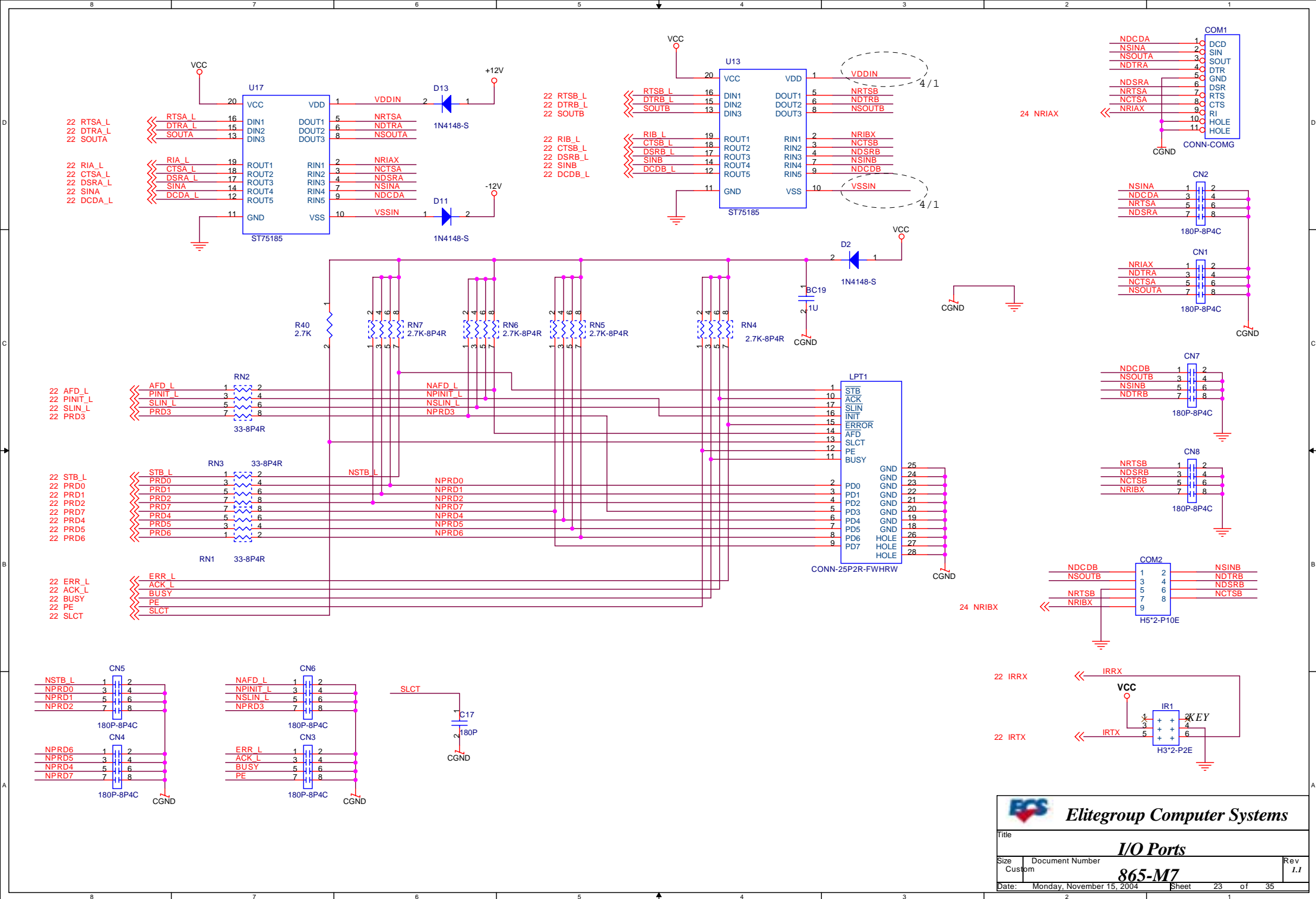


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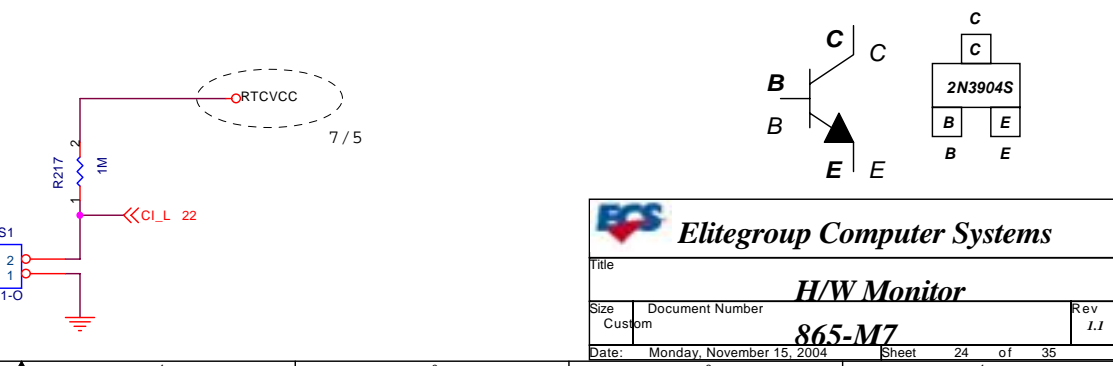
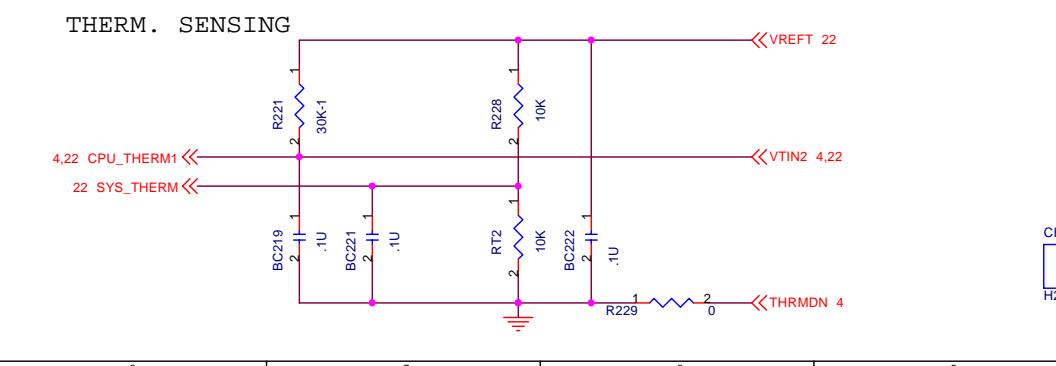
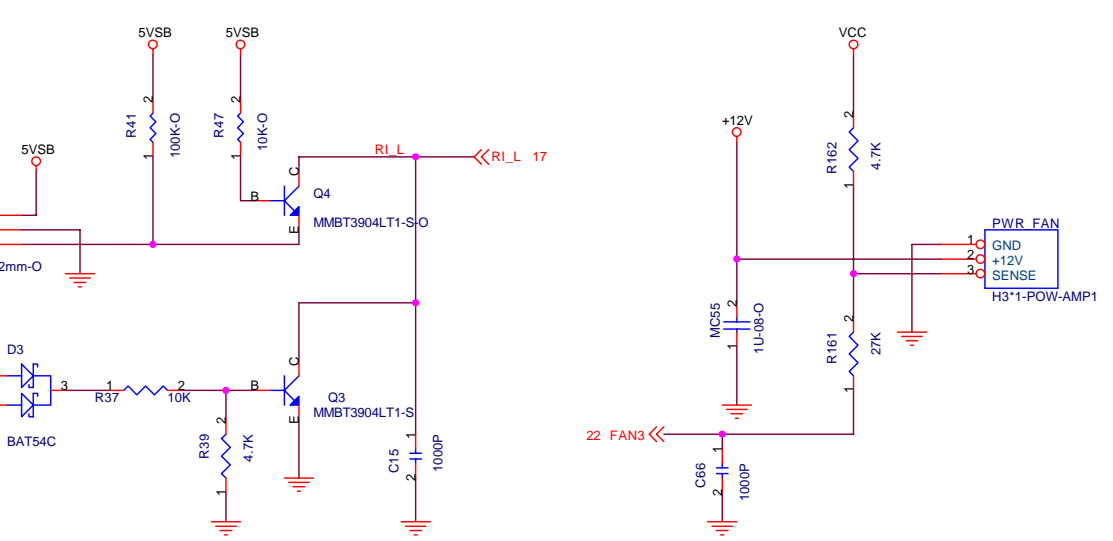
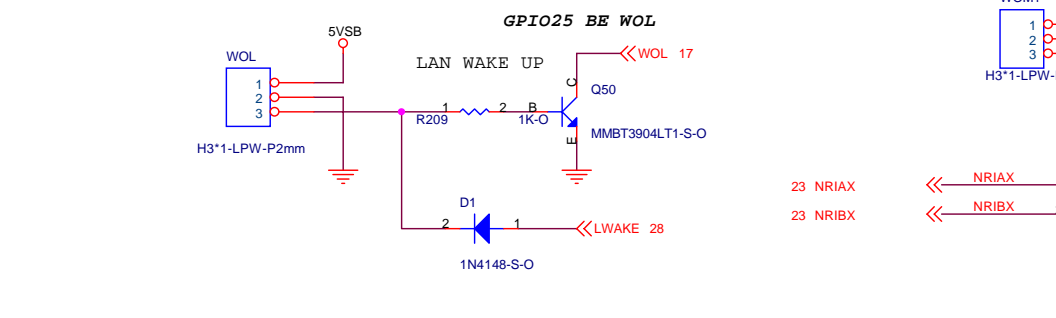
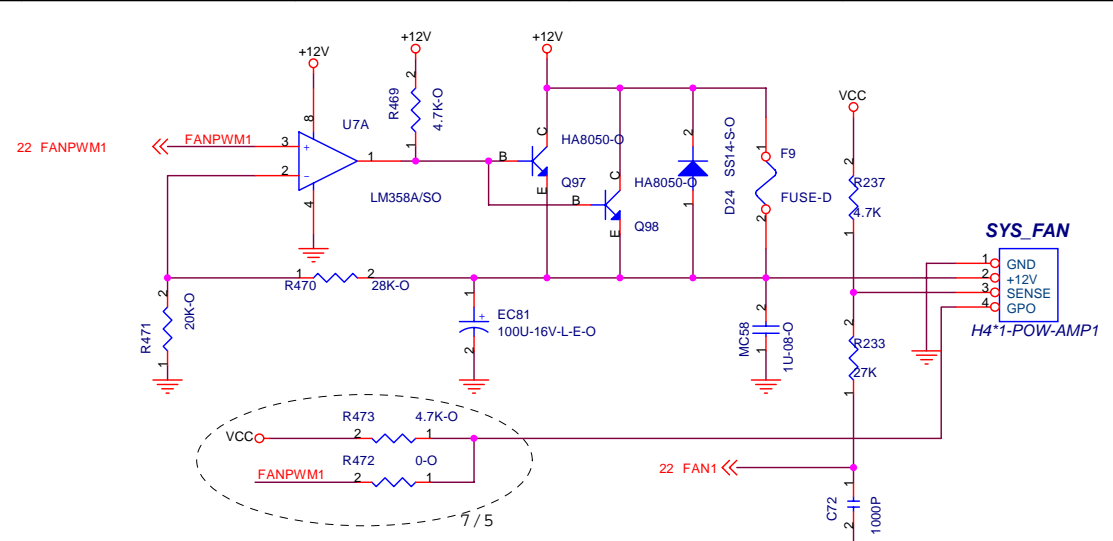
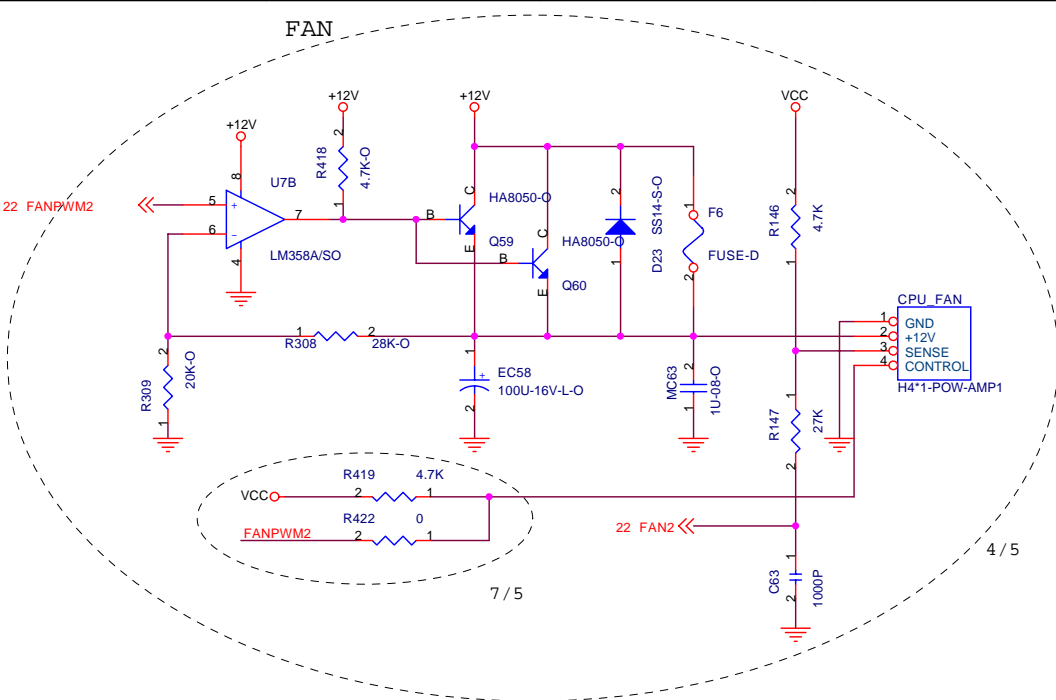
LPC_FDD/KB/M

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FAN



Elitegroup Computer Systems

Title: **H/W Monitor**

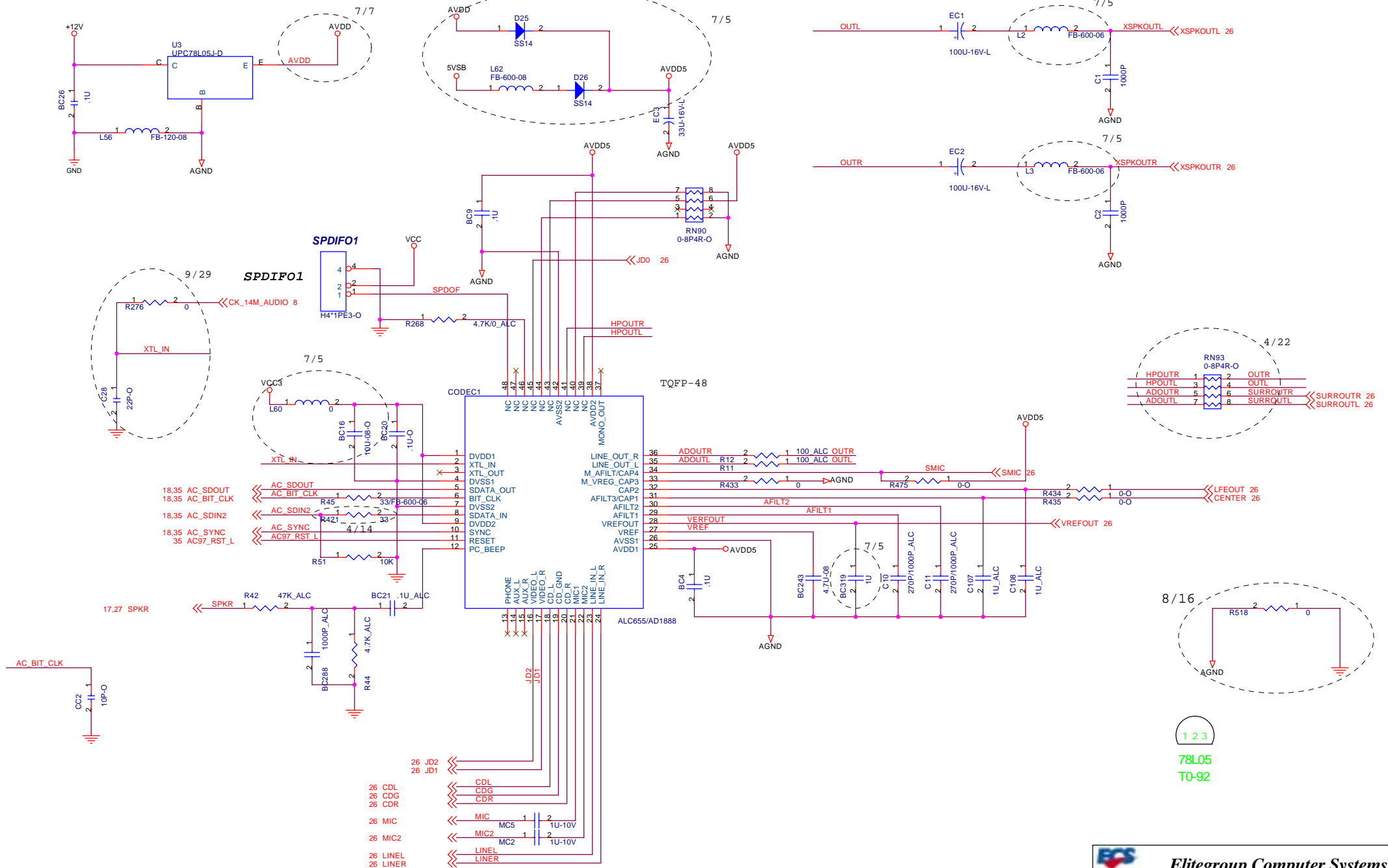
Size: Custom

Document Number: **865-M7**

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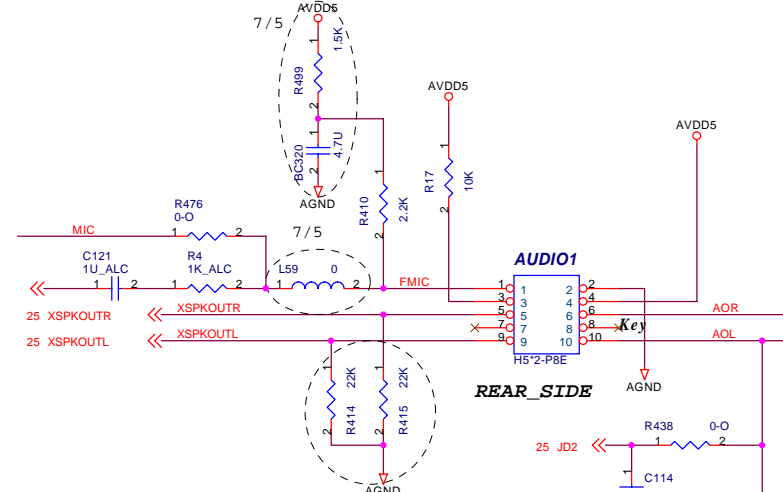
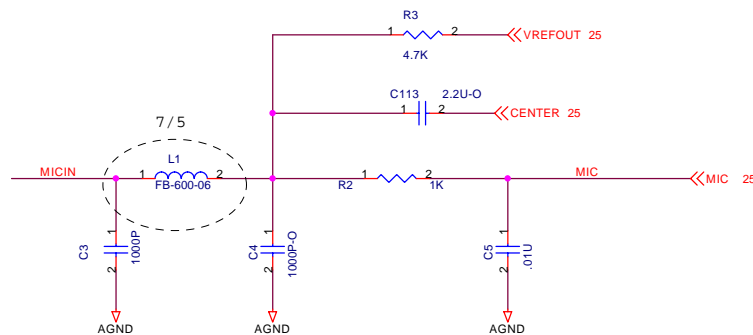
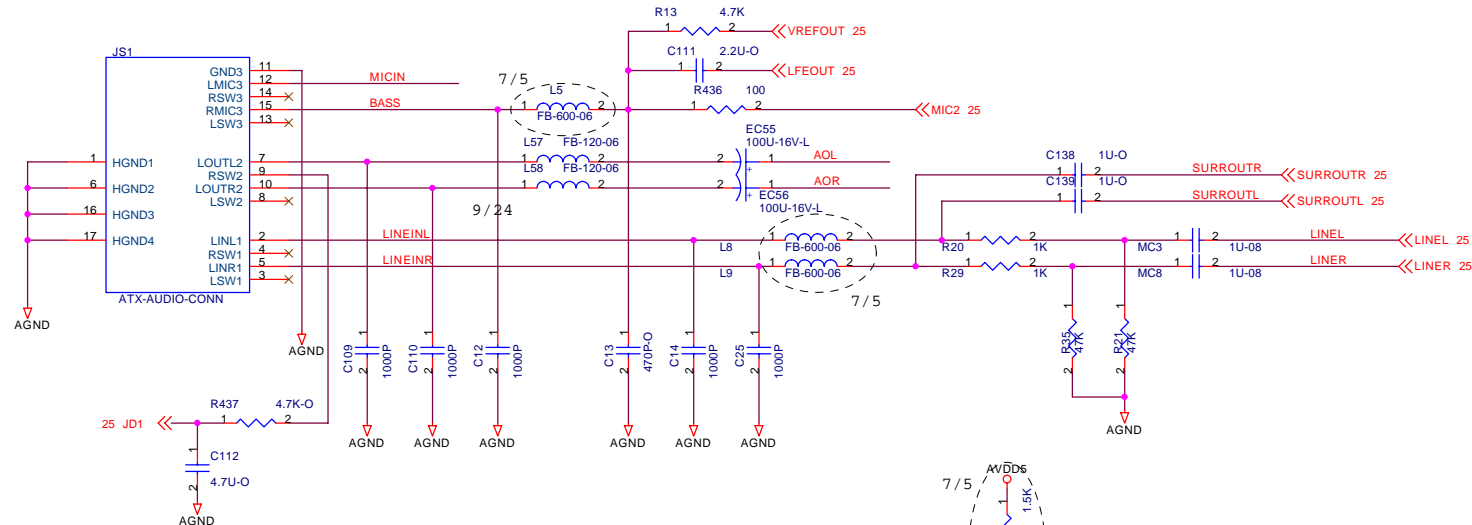
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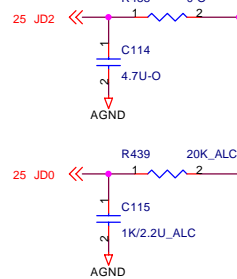
1 2 3

78L05
T0-92

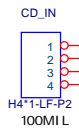


REAR_SIDE

AUDIO1



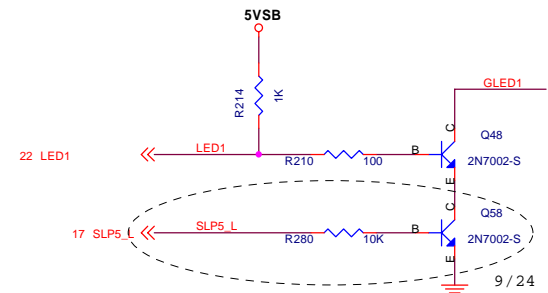
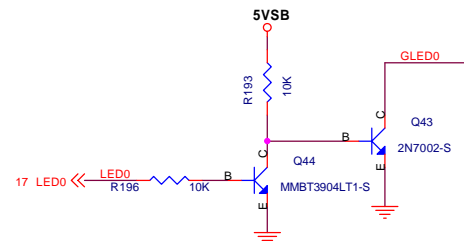
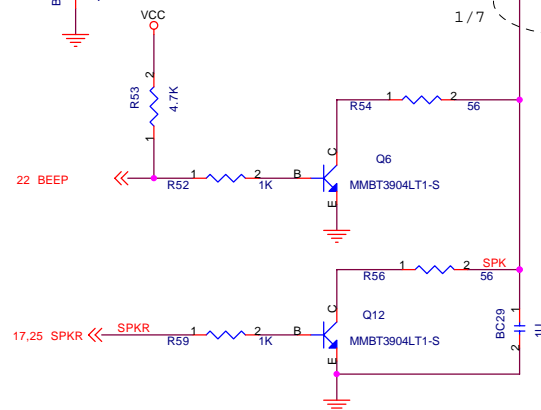
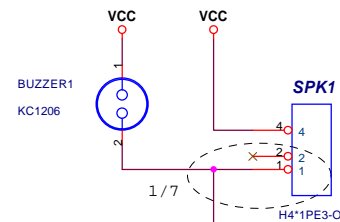
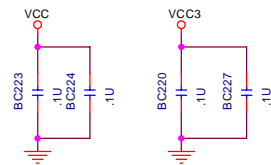
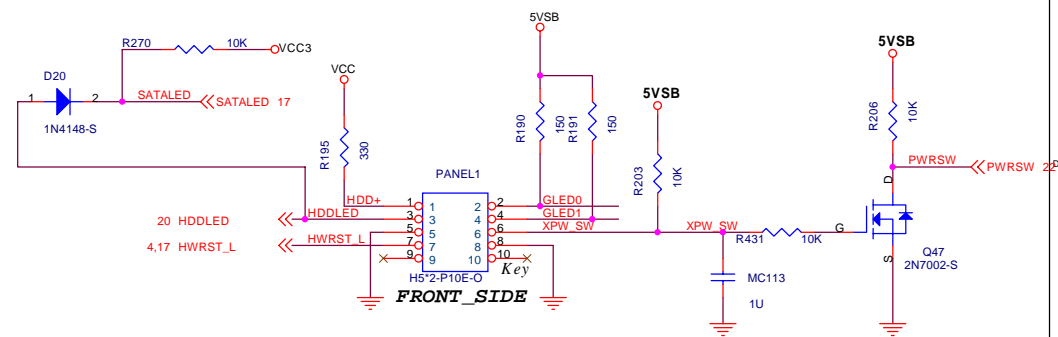
SONY
Big Black
10-341-004008
(2.5mm)

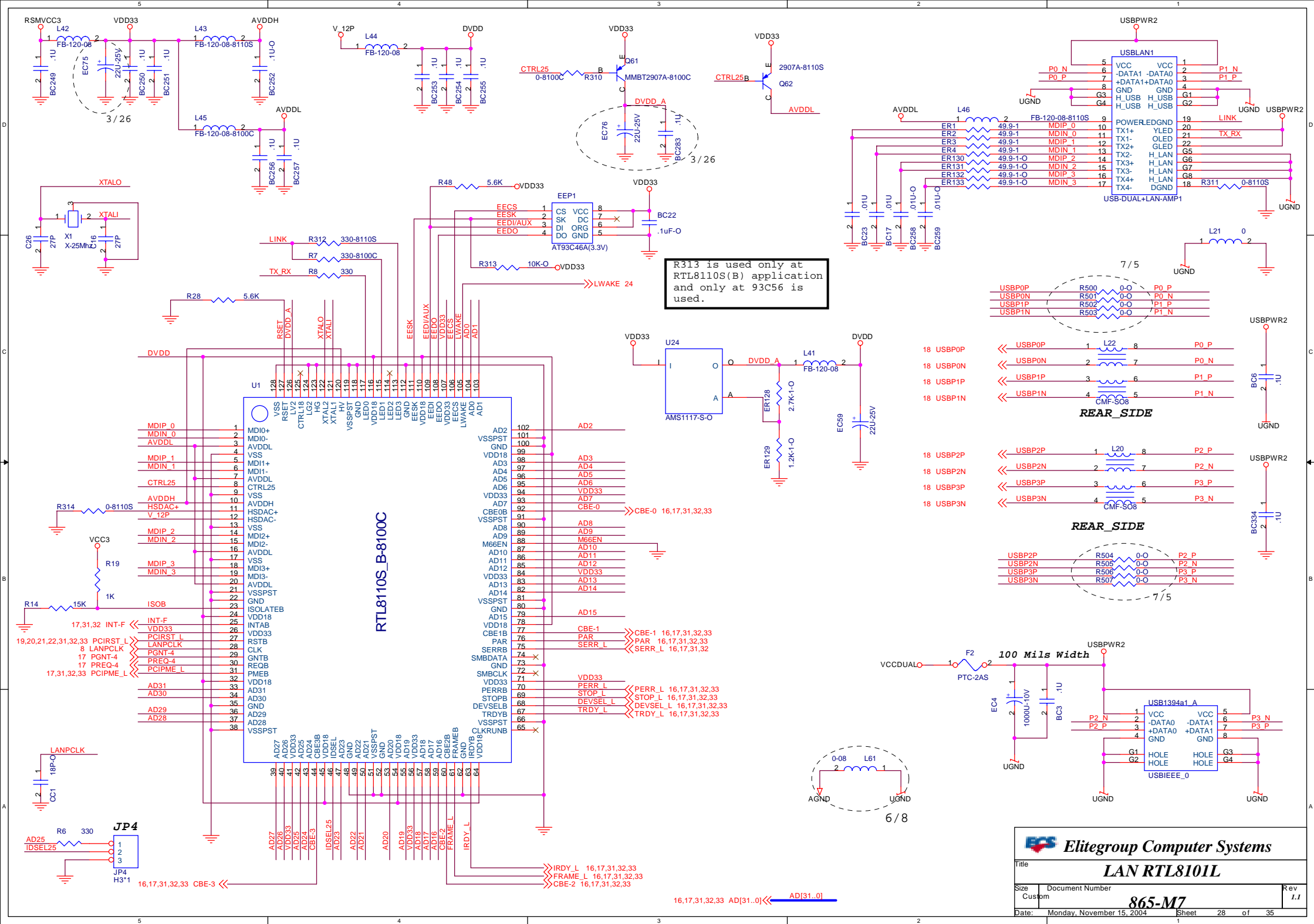


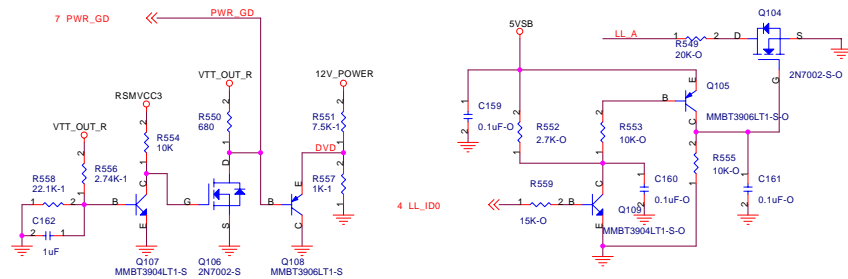
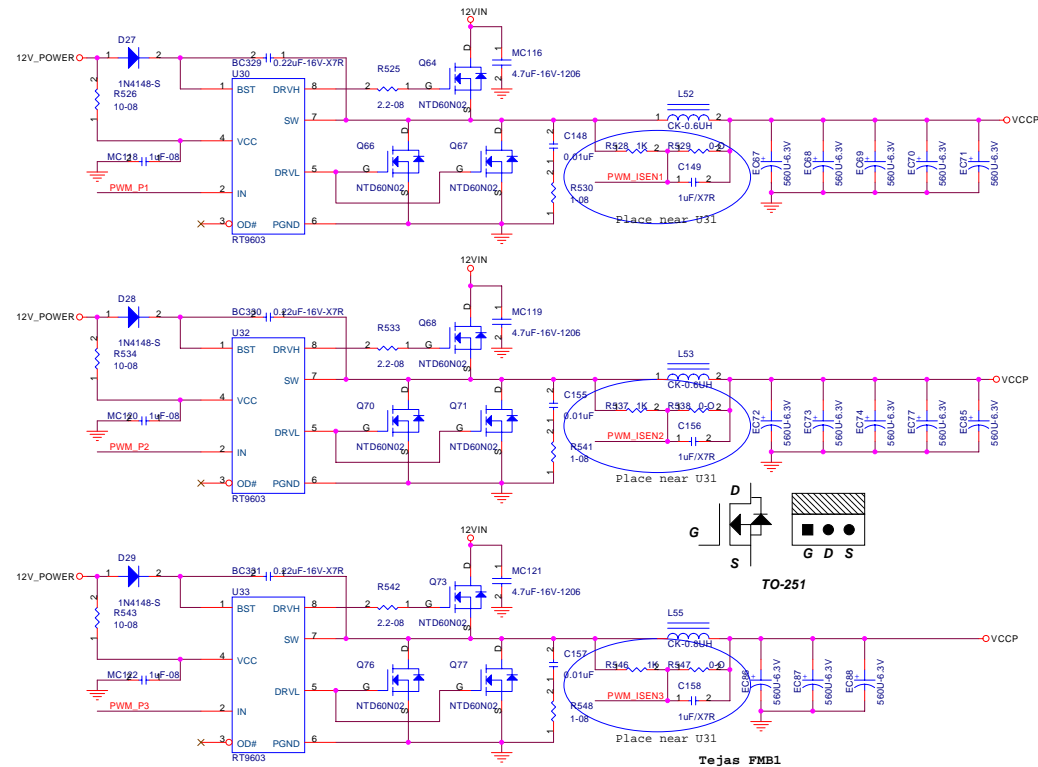
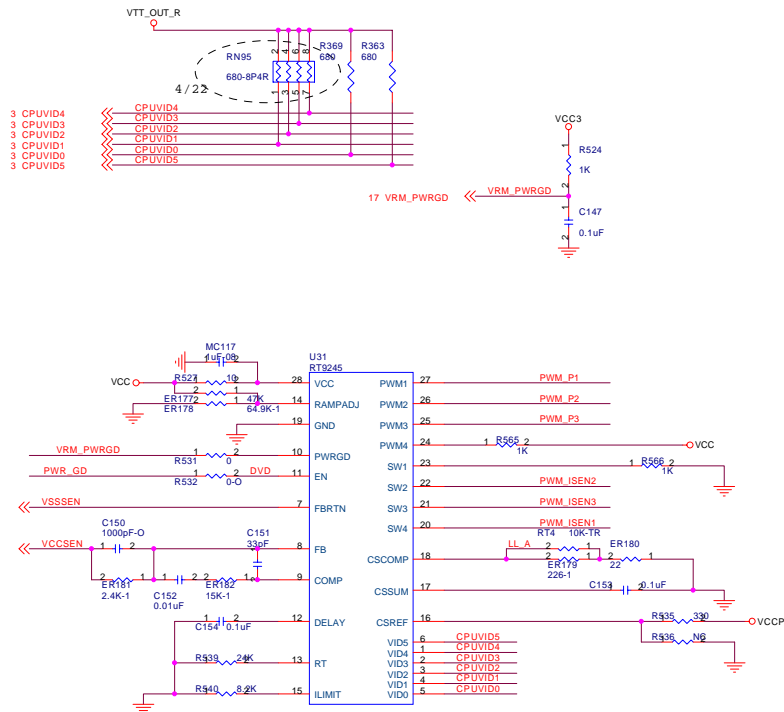
Top view

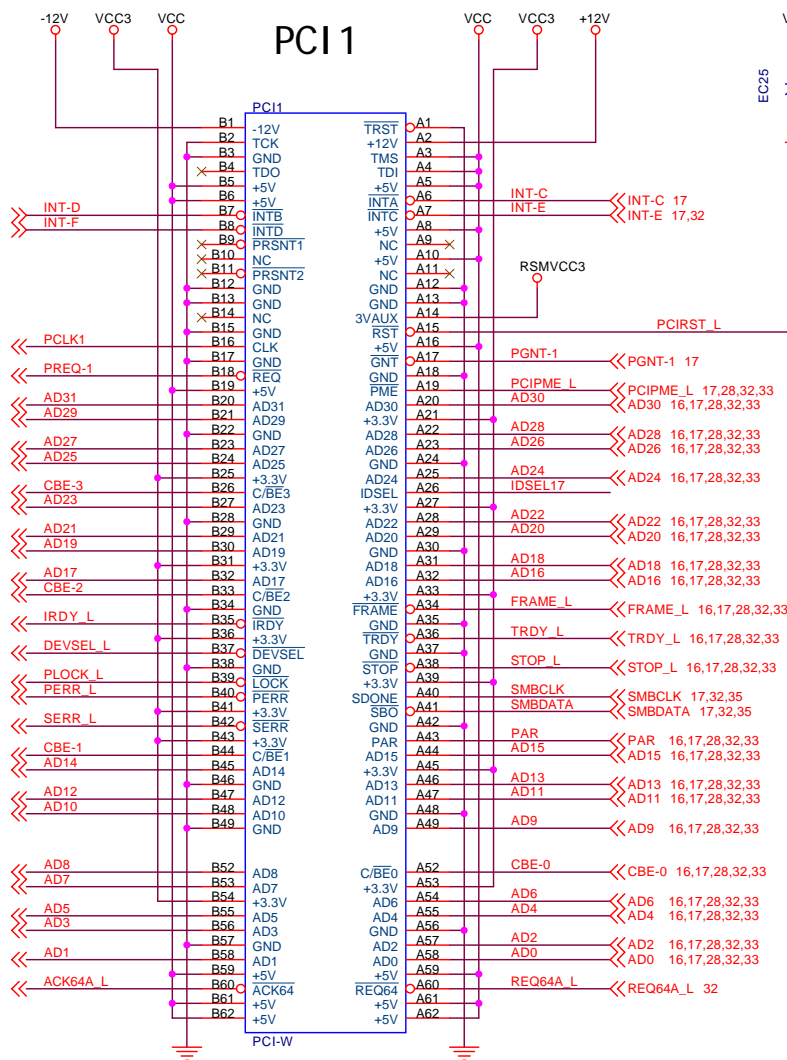


Big / Small

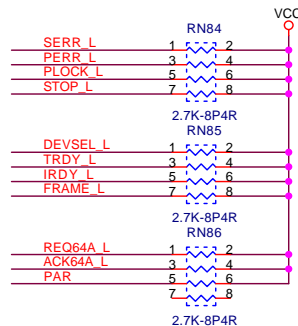
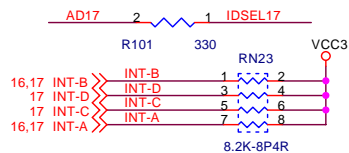




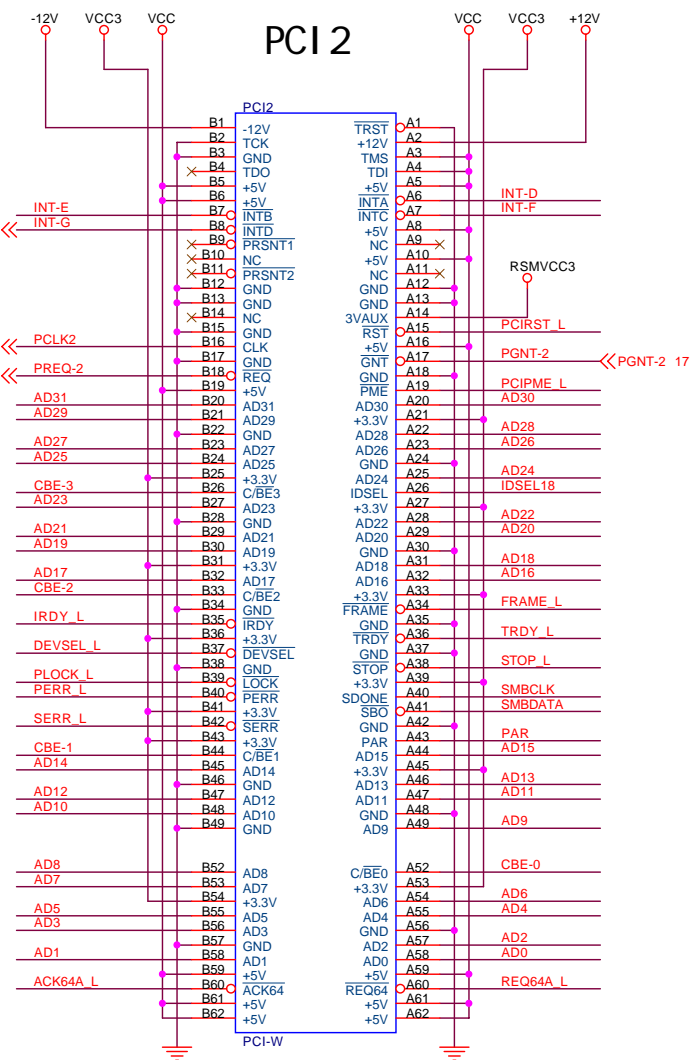




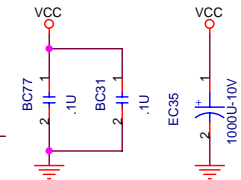
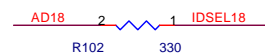
PCI1-INT:
 INTA:INTC
 INTB:INTD
 INTC:INTE
 INTD:INTF



16,17,28,32,33 AD[31..0]



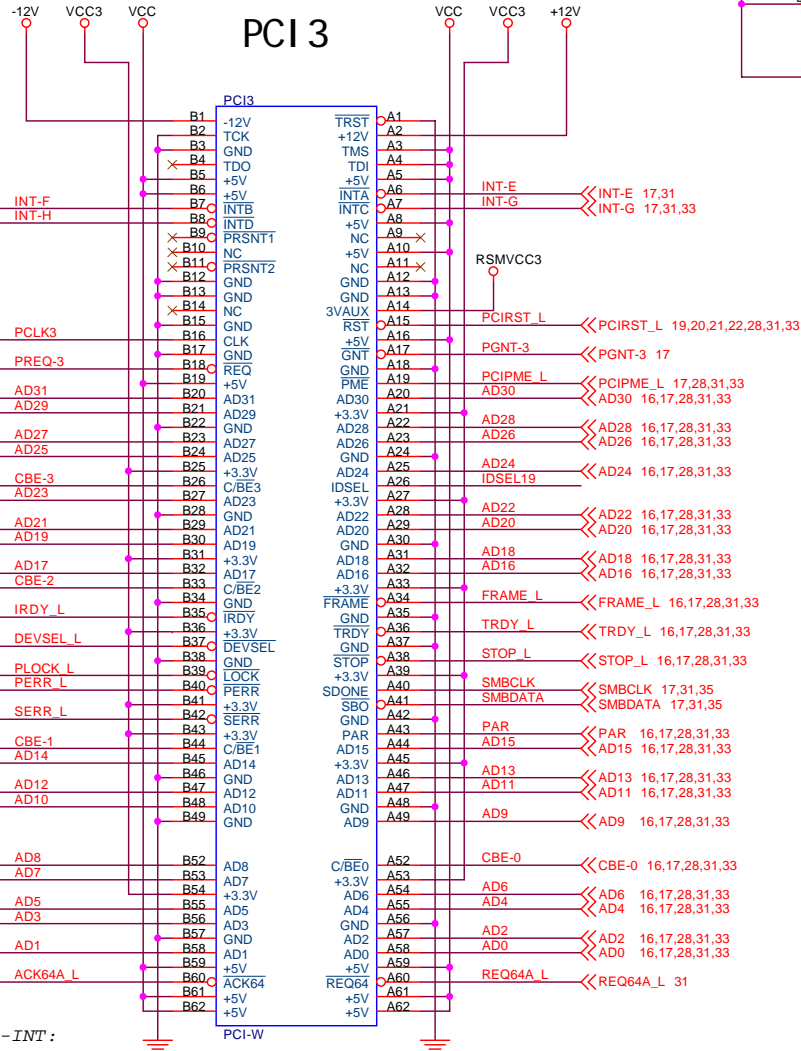
PCI2-INT:
 INTA:INTD
 INTB:INTE
 INTC:INTF
 INTD:INTG



16,17,28,31,33 AD[31..0]

AD[31..0]

PCI 3



PCI3-INT:

INTA: INTE

INTB: INTF

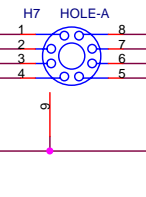
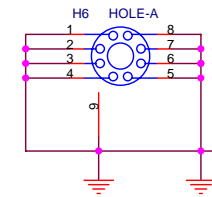
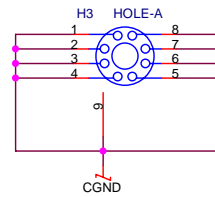
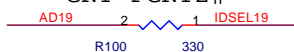
INTC: INTG

INTD: INTH

IDSEL=AD18

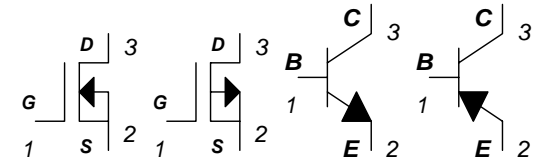
REQ=PREQ2#

GNT=PGNT2#

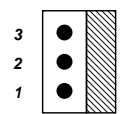


Top View

SOT-23



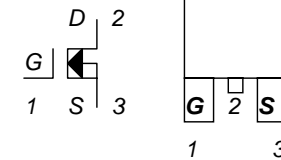
Fan Header



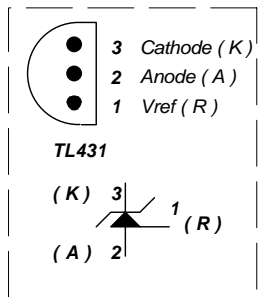
AMP640456-3



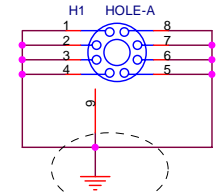
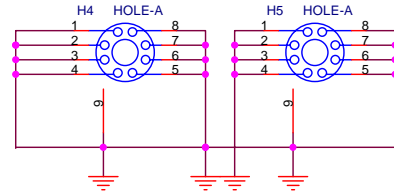
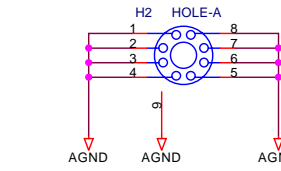
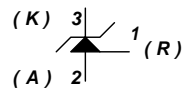
TO-263 / TO-252



Top View



TL431



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Elitegroup Computer Systems		
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